

SQPF9N90B

900V 9A N-Channel Enhancement Mode Power MOSFET

Features

- Low on-Resistance: $R_{DS(on)}=0.80\Omega(\text{typ.})$
- Special Process Technology for high ESD Capability
- 100% Avalanche Test
- Good Stability and Uniformity with High E_{AS}

Applications

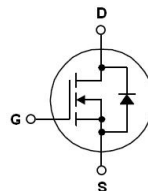
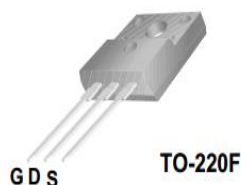
- UPS Applications
- DC-DC Converters and AC-DC Power Supply

Description

The SQPF9N90B is an N-Channel enhancement mode power MOSFET which using proprietary planar stripe and DMOS technology.

This MOSFET has low static on-resistance and high avalanche energy strength. This device provide excellent switching performance for UPS,DC-DC converters and AC-DC power supply.

Package Type & Internal Circuit



Absolute Maximum Ratings @ $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter		Ratings	Unit
V _{DSS}	Drain to Source Voltage		900	V
V _{GSS}	Gate to Source Voltage		±30	V
I _D	Drain Current	T _C =25°C	9	A
		T _C =100°C	5.8	A
I _{DM}	Pulsed Drain Current (Note1)		36	A
P _D	Maximum Power Dissipation	T _C =25°C	69	W
	Derate above 25°C		0.56	W/°C
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		1240	mJ
T _J	Operating Junction Temperature Range		-55~+150	°C
T _{STG}	Storage Temperature Range		-55~+150	°C

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{th(J-C)}$	Thermal Resistance, Junction to case	1.8	$^\circ\text{C}/\text{W}$
$R_{th(J-A)}$	Thermal Resistance, Junction to Ambient	40	$^\circ\text{C}/\text{W}$



Electrical Characteristics @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	900	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	3.0	3.65	5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =4.5A	-	0.8	-	Ω
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =900V, V _{GS} =0V	-	-	10	uA
I _{GSS}	Gate to Source Leakage Current	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA

D-S Diode Characteristics and Maximum Rating @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Maximum Drain to Source Diode Forward Current		-	-	9	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} =0V, I _S =9A	-	0.84	1.0	V
t _{rr}	Reverse Recovery Time	V _{GS} =0V, I _S =9A,	-	0.5	-	us
Q _{rr}	Reverse Recovery Charge	di/dt=-100A/us	-	6.4	-	uC

Switching Characteristics @T_C=25 °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
t _{d(on)}	Turn-on Delay Time	I _D =9A, V _{DD} =450V, R _G =25Ω (Note 3)	-	50	105	ns
t _r	Rising Time		-	115	245	ns
t _{d(off)}	Turn-off Delay Time		-	95	200	ns
t _f	Falling Time		-	70	155	ns
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, f=1.0MHz	-	-	2720	pF
C _{oss}	Output Capacitance		-	-	220	pF
C _{rss}	Reverse Transfer Capacitance		-	-	18	pF
Q _g	Total Gate Charge	I _D =9A, V _{DS} =720V V _{GS} =10V (Note 3)	-	43	-	nC
Q _{gs}	Gate to Source Charge		-	11	-	nC
Q _{gd}	Gate to Drain Charge		-	16	-	nC

- Note:**
1. Repetitive rating: pulse-width limited by maximum junction temperature
 2. L=5mH, V_{DD}=100V, V_G=10V, @T_C=25°C
 3. Essentially independent of operating temperature typical characteristics

Typical Performance Characteristics

Fig. 1. Typical on-Region Characteristics

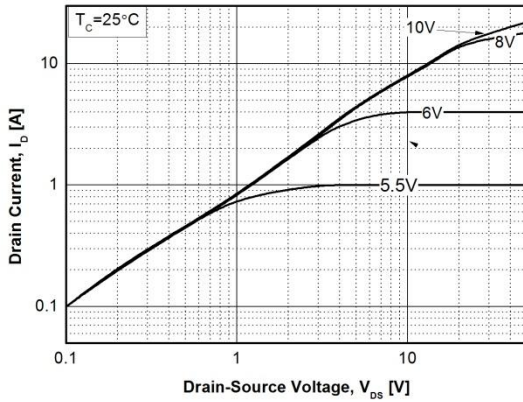


Fig. 2. Typical Transfer Characteristics

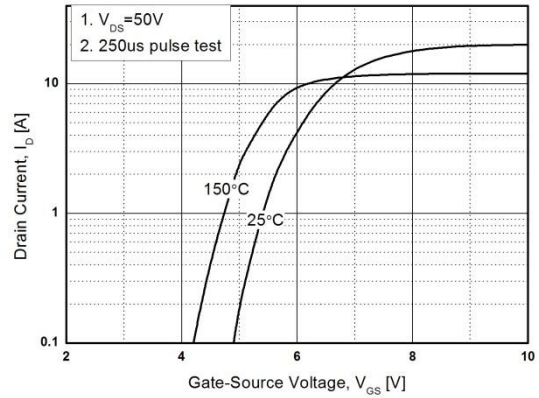


Fig. 3. Static on-Resistance vs. I_D

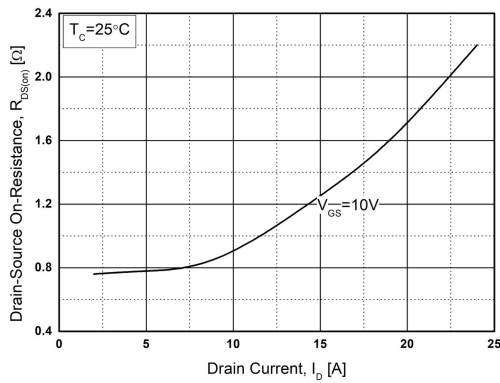


Fig. 4. Body Diode Forward Voltage vs. I_{DR}

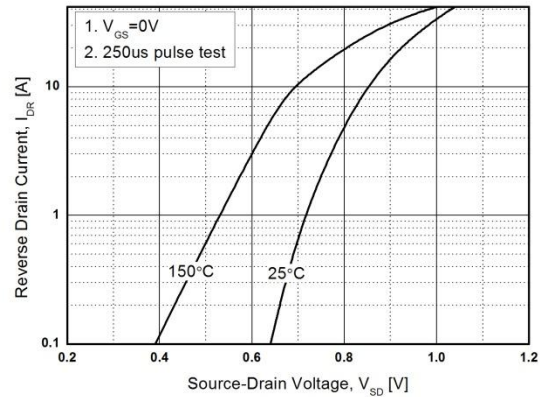


Fig. 5. Capacitance Characteristics

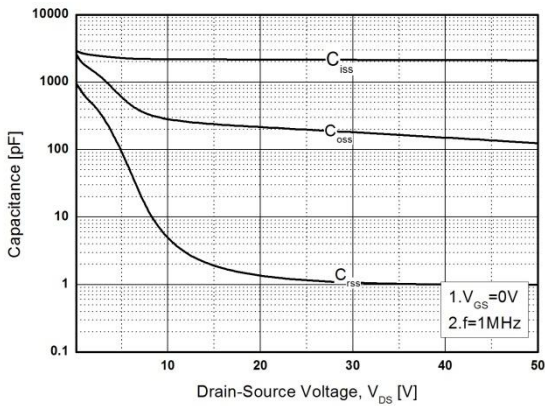
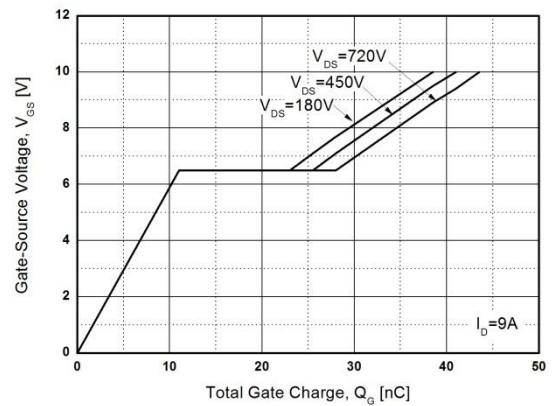


Fig. 6. Gate Charge Characteristics



Typical Performance Characteristics

Fig. 7. Breakdown Voltage vs. Temperature

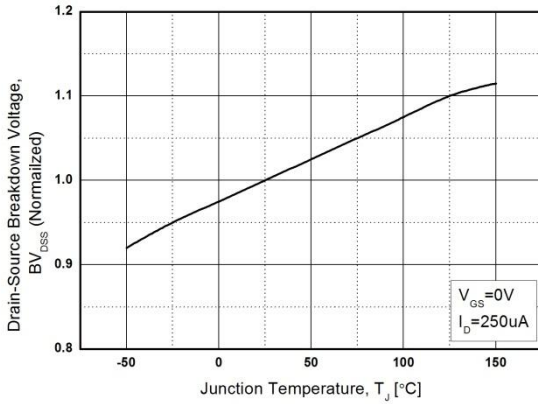


Fig. 8. Static on-Resistance vs. Temperature

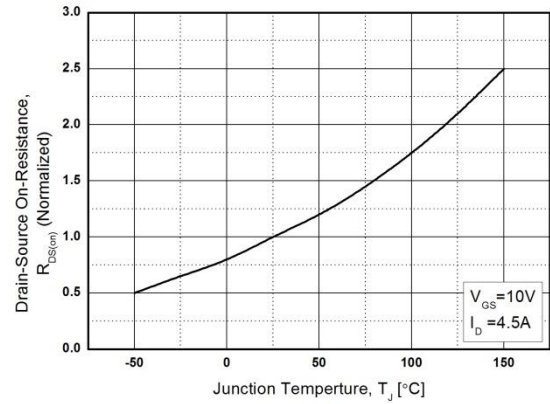


Fig. 9. Maximum Safe Operating Area

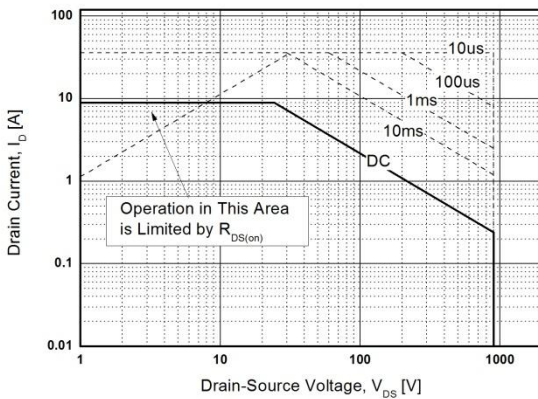


Fig. 10. Maximum Drain Current vs. Temperature

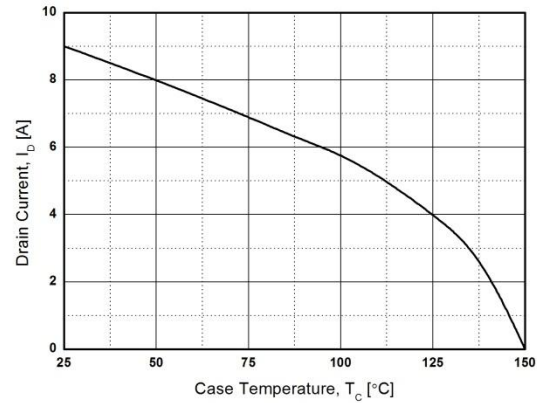
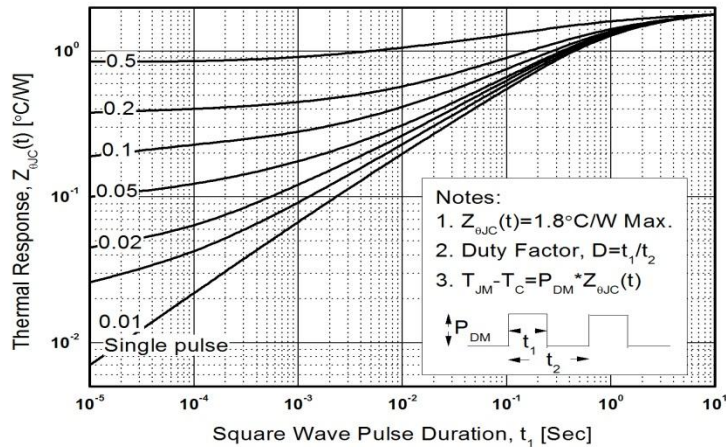


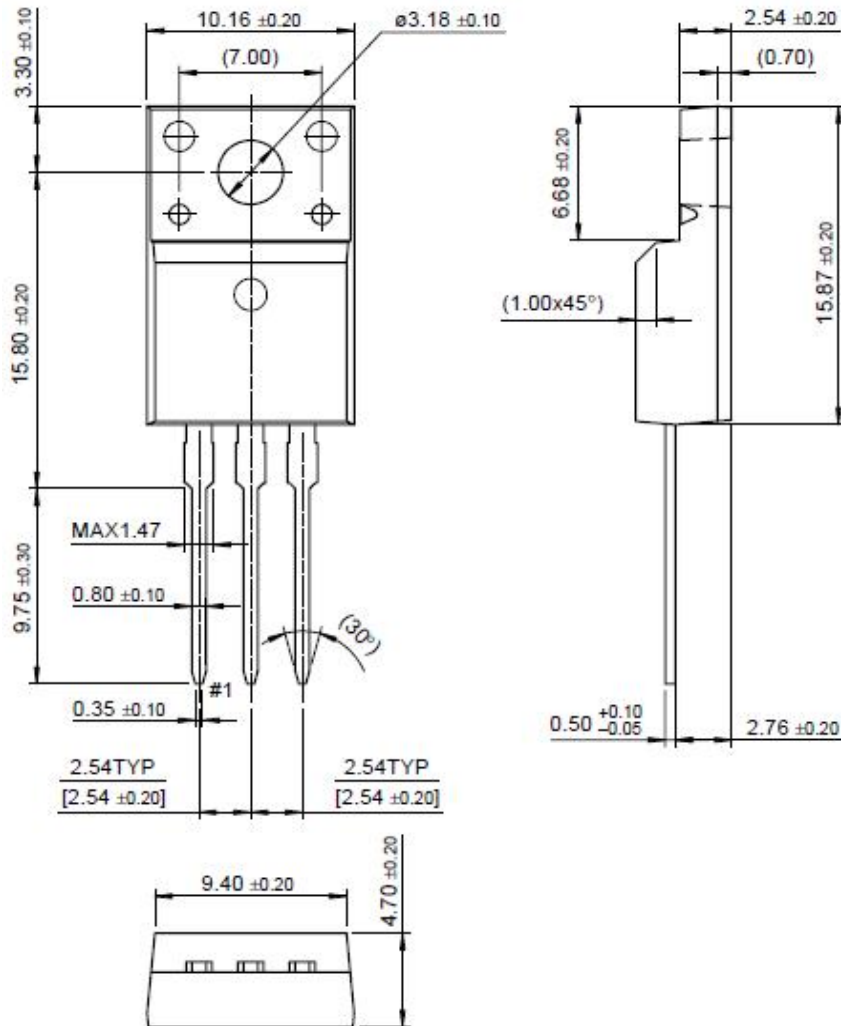
Fig. 11. Transient Thermal Response Curve



Package Dimensions

TO-220F

(Dimensions in Millimeters)



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